

Circuits At The Nanoscale Communications Imaging And Sensing Devices Circuits And Systems

Device and Circuit Techniques for Reducing Variation in Nanoscale SRAM
Multi-voltage Nanoscale CMOS Circuit Techniques
High-Speed Optical Receivers with Integrated Photodiode in Nanoscale CMOS
Circuits at the Nanoscale
Introduction to Semiconductor Lasers for Optical Communications
MIMO Power Line Communications
Analog Circuit Design for Embedded and High Performance Processors in Nanoscale Technologies
Smart Grid
Semiconductor Radiation Detection Systems
Cellular Nanoscale Sensory Wave Computing
Nanoscale Devices
IEEE Transactions on Circuits and Systems
IEEE Circuits & Devices
CMOS Processors and Memories
Nano-scale CMOS Analog Circuits
Plasmonic Nanoguides and Circuits
Mobile and Wireless Communications
Journal of Computational and Theoretical Nanoscience
Testing for Small-Delay Defects in Nanoscale CMOS Integrated Circuits
Nanoelectronics, Circuits and Communication Systems
Reliability of Nanoscale Circuits and Systems
Nanophotonics
Nanotechnology for Telecommunications
Mobile Communication and Power Engineering
CMOS Nanoelectronics: Analog and RF VLSI Circuits
Nanoscale VLSI
Internet Networks
Journal of Nanoscience and Nanotechnology
High-Frequency Integrated Circuits
Low Power Circuits for Emerging Applications in Communications, Computing, and

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SensingNanoscale TransistorsPower-efficient Ultra-high-speed Communication Circuit Techniques in Nanoscale CMOSLow-Power High-Level Synthesis for Nanoscale CMOS CircuitsAdvanced Semiconductor and Organic Nano-techniques: Nanoscale electronics and optoelectronicsMedical ImagingIntroduction to Nanoscale Science and TechnologyElectronics for Radiation DetectionThe British National BibliographyNanoelectronicsComprehensive Nanoscience and Technology

Device and Circuit Techniques for Reducing Variation in Nanoscale SRAM

Multi-voltage Nanoscale CMOS Circuit Techniques

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process

limitations affecting circuit performance.

High-Speed Optical Receivers with Integrated Photodiode in Nanoscale CMOS

The primary aim of this book is to discuss various aspects of nanoscale device design and their applications including transport mechanism, modeling, and circuit applications. Furthermore, the book develops a strong foundation to understand the need for moving from conventional MOSFET to novel devices, including, how the device physics and transport phenomenon changes with reduction in the device size to a nanoscale regime. Details about the simulation technique and/or fabrication process flow of the various nanoscale devices is included along with simulated results of device performance parameters. The numerical and theoretical methods are used to describe the related concepts.

Circuits at the Nanoscale

Preface. Introduction. I: Nanoscale Fabrication and Characterization. 1. Nanolithography; L.R. Harriott, R. Hull. 2. Self-Assembly And Self-Organization; R. Shenhar, T.B. Norsten, V.M. Rotello. 3. Scanning Probe Microscopes; K.-W. Ng. II: Nanomaterials and Nanostructures. 4. The Geometry of Nanoscale Carbon; V. Crespi. 5. Fullerenes; H. Dorn, J.C. Duchamp. 6. Carbon Nanotubes; B.W. Smith, D.E. Luzzi. 7. Quantum Dots; A.B. Denison, L.J. Hope-Weeks, R.W. Meulenber, L.J. Terminello. 8.

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Nanocomposites; R.C. Cammarata. III: Nanoscale and Molecular Electronics. 9. Advances In Microelectronics - From Microscale To Nanoscale Devices; J. Van Der Spiegel. 10. Molecular Electronics; M. Zwolak, M. Di Ventra. 11. Single Electron Transistors; J.G. Lu. IV: Nanotechnology in Magnetic Systems. 12. Semiconductor Nanostructures For Quantum Computation; M.E. Flatté. 13. Magnetoresistive Materials And Devices; O. Heinonen. 14. Nanotechnology In Magnetic Storage; J.A. Katine, R.E. Fontana Jr. V: Nanotechnology in Integrative Systems. 15. Introduction To Integrative Systems; M. Gaitan. 16. Nanoelectromechanical Systems; S. Evoy, M. Duemling, T. Jaruhar. 17. Micromechanical Sensors; P. Datskos, N.V. Lavrik. VI: Nanoscale Optoelectronics. 18. Quantum-Confined Optoelectronic Systems; S. Fafard. 19. Organic Optoelectronic Nanostructures; J.R. Heflin. 20. Photonic Crystals; Y. Xia, K. Kamata, Y. Lu. VII: Nanobiotechnology. 21. Biomemetic Nanostructures; D.E. Dische. 22. Biomolecular Motors; J. Schmidt, C. Montemagno. 23. Nanofluidics; J. Han.

Introduction to Semiconductor Lasers for Optical Communications

This book features selected papers presented at Third International Conference on Nanoelectronics, Circuits and Communication Systems (NCCS 2017). Covering topics such as MEMS and nanoelectronics, wireless communications, optical communication, instrumentation, signal processing, Internet of Things, image processing, bioengineering, green energy, hybrid vehicles, environmental science, weather

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forecasting, cloud computing, renewable energy, RFID, CMOS sensors, actuators, transducers, telemetry systems, embedded systems, and sensor network applications in mines, it is a valuable resource for young scholars, researchers, and academics.

MIMO Power Line Communications

This book investigates the behavior of light (light pulse) within the micro- and nano-scale device (ring resonator), which can be integrated to form the device, circuits, and systems that can be used for atom/molecule trapping and transportation, optical transistor, fast calculation devices (optical gate), nanoscale communication and networks, and energy storage, etc. The large demand of small-scale device, especially, with light signal processing is needed. This book discusses device (nano device) design and simulation, which can be useful for practice in the near future.

Analog Circuit Design for Embedded and High Performance Processors in Nanoscale Technologies

Semiconductor Radiation Detection Systems addresses the state-of-the-art in the design of semiconductor detectors and integrated circuit design, in the context of medical imaging using ionizing radiation. It addresses exciting new opportunities in X-ray detection, Computer Tomography (CT), bone dosimetry, and nuclear

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medicine (PET, SPECT). In addition to medical imaging, the book explores other applications of semiconductor radiation detection systems in security applications such as luggage scanning, dirty bomb detection, and border control. Features a chapter written by well-known Gamma-Ray Imaging authority Tadayuki Takahashi Assembled by a combination of top industrial experts and academic professors, this book is more than just a product manual. It is practical enough to provide a solid explanation of presented technologies, incorporating material that offers an optimal balance of scientific and academic theory. With less of a focus on math and physical details, the author concentrates more on exploring exactly how technologies are being used. With its combined coverage of new materials and innovative new system approaches, as well as a succinct overview of recent developments, this book is an invaluable tool for any engineer, professional, or student working in electronics or an associated field.

Smart Grid

One of the first publications of its kind in the exciting field of multiple input multiple output (MIMO) power line communications (PLC), MIMO Power Line Communications: Narrow and Broadband Standards, EMC, and Advanced Processing contains contributions from experts in industry and academia, making it practical enough to provide a solid understanding of how PLC technologies work, yet scientific enough to form a base for ongoing R&D activities. This book is subdivided into five thematic parts. Part I looks at

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narrow- and broadband channel characterization based on measurements from around the globe. Taking into account current regulations and electromagnetic compatibility (EMC), part II describes MIMO signal processing strategies and related capacity and throughput estimates. Current narrow- and broadband PLC standards and specifications are described in the various chapters of part III. Advanced PLC processing options are treated in part IV, drawing from a wide variety of research areas such as beamforming/precoding, time reversal, multi-user processing, and relaying. Lastly, part V contains case studies and field trials, where the advanced technologies of tomorrow are put into practice today. Suitable as a reference or a handbook, MIMO Power Line Communications: Narrow and Broadband Standards, EMC, and Advanced Processing features self-contained chapters with extensive cross-referencing to allow for a flexible reading path.

Semiconductor Radiation Detection Systems

Modern communication systems dealing with huge amounts of data at ever increasing speed try to utilize the best aspects of electronic and optical circuits. Electronic circuits are tiny but their operation speed is limited, whereas optical circuits are extremely fast but their sizes are limited by diffraction. Waveguide components utilizing surface plasmon (SP) modes were found to combine huge optical bandwidth and compactness of electronics, and plasmonics thereby became considered as the next chip-scale technology.

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In this book, we concentrate on the SP waveguide configurations ensuring nanoscale confinement and review the current status of this rapidly emerging field, considering different configurations being developed for nanoscale plasmonic guides and circuits. Both fundamental physics and application aspects of plasmonics are reviewed in detail by world-leading experts. A unique feature of this book is its strong focus on a particular subfield of plasmonics dealing with subwavelength (nanoscale) waveguiding, an area which is especially important in view of explosively growing interest to plasmonic interconnects and nanocircuits. This research direction came to the fore very recently, driven by the ever increasing demand of faster and smaller interconnects to be used inside computer chips, and stimulated by the progress in our understanding of basic physical phenomena involved in SP excitation, imaging and manipulation.

Cellular Nanoscale Sensory Wave Computing

The book addresses the need to investigate new approaches to lower energy requirement in multiple application areas and serves as a guide into emerging circuit technologies. It explores revolutionary device concepts, sensors, and associated circuits and architectures that will greatly extend the practical engineering limits of energy-efficient computation. The book responds to the need to develop disruptive new system architectures, circuit microarchitectures, and attendant device and interconnect technology

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aimed at achieving the highest level of computational energy efficiency for general purpose computing systems. Features Discusses unique technologies and material only available in specialized journal and conferences Covers emerging applications areas, such as ultra low power communications, emerging bio-electronics, and operation in extreme environments Explores broad circuit operation, ex. analog, RF, memory, and digital circuits Contains practical applications in the engineering field, as well as graduate studies Written by international experts from both academia and industry

Nanoscale Devices

IEEE Transactions on Circuits and Systems

IEEE Circuits & Devices

CMOS Processors and Memories

From the Introduction: Nanotechnology and its underpinning sciences are progressing with unprecedented rapidity. With technical advances in a variety of nanoscale fabrication and manipulation technologies, the whole topical area is maturing into a vibrant field that is generating new scientific research and a burgeoning range of commercial applications, with an annual market already at the trillion dollar

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threshold. The means of fabricating and controlling matter on the nanoscale afford striking and unprecedented opportunities to exploit a variety of exotic phenomena such as quantum, nanophotonic and nanoelectromechanical effects. Moreover, researchers are elucidating new perspectives on the electronic and optical properties of matter because of the way that nanoscale materials bridge the disparate theories describing molecules and bulk matter. Surface phenomena also gain a greatly increased significance; even the well-known link between chemical reactivity and surface-to-volume ratio becomes a major determinant of physical properties, when it operates over nanoscale dimensions. Against this background, this comprehensive work is designed to address the need for a dynamic, authoritative and readily accessible source of information, capturing the full breadth of the subject. Its six volumes, covering a broad spectrum of disciplines including material sciences, chemistry, physics and life sciences, have been written and edited by an outstanding team of international experts. Addressing an extensive, cross-disciplinary audience, each chapter aims to cover key developments in a scholarly, readable and critical style, providing an indispensable first point of entry to the literature for scientists and technologists from interdisciplinary fields. The work focuses on the major classes of nanomaterials in terms of their synthesis, structure and applications, reviewing nanomaterials and their respective technologies in well-structured and comprehensive articles with extensive cross-references. It has been a constant surprise and delight to have found, amongst the rapidly escalating number who work in nanoscience and technology, so

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many highly esteemed authors willing to contribute. Sharing our anticipation of a major addition to the literature, they have also captured the excitement of the field itself in each carefully crafted chapter. Along with our painstaking and meticulous volume editors, full credit for the success of this enterprise must go to these individuals, together with our thanks for (largely) adhering to the given deadlines. Lastly, we record our sincere thanks and appreciation for the skills and professionalism of the numerous Elsevier staff who have been involved in this project, notably Fiona Geraghty, Megan Palmer and Greg Harris, and especially Donna De Weerd-Wilson who has steered it through from its inception. We have greatly enjoyed working with them all, as we have with each other.

Nano-scale CMOS Analog Circuits

Reliability concerns and the limitations of process technology can sometimes restrict the innovation process involved in designing nano-scale analog circuits. The success of nano-scale analog circuit design requires repeat experimentation, correct analysis of the device physics, process technology, and adequate use of the knowledge database. Starting with the basics, *Nano-Scale CMOS Analog Circuits: Models and CAD Techniques for High-Level Design* introduces the essential fundamental concepts for designing analog circuits with optimal performances. This book explains the links between the physics and technology of scaled MOS transistors and the design and simulation of nano-scale analog circuits. It also explores the development of

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structured computer-aided design (CAD) techniques for architecture-level and circuit-level design of analog circuits. The book outlines the general trends of technology scaling with respect to device geometry, process parameters, and supply voltage. It describes models and optimization techniques, as well as the compact modeling of scaled MOS transistors for VLSI circuit simulation. • Includes two learning-based methods: the artificial neural network (ANN) and the least-squares support vector machine (LS-SVM) method • Provides case studies demonstrating the practical use of these two methods • Explores circuit sizing and specification translation tasks • Introduces the particle swarm optimization technique and provides examples of sizing analog circuits • Discusses the advanced effects of scaled MOS transistors like narrow width effects, and vertical and lateral channel engineering Nano-Scale CMOS Analog Circuits: Models and CAD Techniques for High-Level Design describes the models and CAD techniques, explores the physics of MOS transistors, and considers the design challenges involving statistical variations of process technology parameters and reliability constraints related to circuit design.

Plasmonic Nanoguides and Circuits

Mobile and Wireless Communications

Physical sciences and engineering, as well as biological sciences have recently made great strides in their respective fields. More importantly, the cross-

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fertilization of ideas, paradigms and methodologies have led to the unprecedented technological developments in areas such as information processing, full colour semiconductor displays, compact biosensors and controlled drug discovery to name a few. Top experts in their respective fields have come together to discuss the latest developments and the future of micro-nano electronics. They investigate issues to be faced in ultimate limits such as single electron transistors; zero dimensional systems for unique properties; thresholdless lasers, electronics based on inexpensive and flexible plastic chips; cell manipulation; biosensors; DNA based computers; quantum computing; DNA sequencing chips; micro fluidics; nanomotors based on molecules; molecular electronics and recently emerging wide bandgap semiconductors for emitters, detectors and power amplifiers. Contributions from top experts in this field Covers a wide range of topics

Journal of Computational and Theoretical Nanoscience

In the not too distant future, internet access will be dominated by wireless networks. With that, wireless edge using optical core next-generation networks will become as ubiquitous as traditional telephone networks. This means that telecom engineers, chip designers, and engineering students must prepare to meet the challenges and opportunities that the development and deployment of these technologies will bring. Bringing together cutting-edge coverage of

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wireless and optical networks in a single volume, Internet Networks Wired, Wireless, and Optical Technologies provides a concise yet complete introduction to these dynamic technologies. Filled with case studies, illustrations, and practical examples from industry, the text explains how wireless, wireline, and optical networks work together. It also: Covers WLAN, WPAN, wireless access, 3G/4G cellular, RF transmission Details optical networks involving long-haul and metropolitan networks, optical fiber, photonic devices, and VLSI chips Provides clear instruction on the application of wireless and optical networks Taking into account recent advances in storage, processing, sensors, displays, statistical data analyses, and autonomic systems, this reference provides forward thinking engineers and students with a realistic vision of how the continued evolution of the technologies that touch wireless communication will soon reshape markets and business models around the world.

Testing for Small-Delay Defects in Nanoscale CMOS Integrated Circuits

Mobile and wireless communications applications have a clear impact on improving the humanity wellbeing. From cell phones to wireless internet to home and office devices, most of the applications are converted from wired into wireless communication. Smart and advanced wireless communication environments represent the future technology and evolutionary development step in homes, hospitals, industrial, vehicular and transportation systems. A

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very appealing research area in these environments has been the wireless ad hoc, sensor and mesh networks. These networks rely on ultra low powered processing nodes that sense surrounding environment temperature, pressure, humidity, motion or chemical hazards, etc. Moreover, the radio frequency (RF) transceiver nodes of such networks require the design of transmitter and receiver equipped with high performance building blocks including antennas, power and low noise amplifiers, mixers and voltage controlled oscillators. Nowadays, the researchers are facing several challenges to design such building blocks while complying with ultra low power consumption, small area and high performance constraints. CMOS technology represents an excellent candidate to facilitate the integration of the whole transceiver on a single chip. However, several challenges have to be tackled while designing and using nanoscale CMOS technologies and require innovative idea from researchers and circuits designers. While major researchers and applications have been focusing on RF wireless communication, optical wireless communication based system has started to draw some attention from researchers for a terrestrial system as well as for aerial and satellite terminals. This renewed interested in optical wireless communications is driven by several advantages such as no licensing requirements policy, no RF radiation hazards, and no need to dig up roads besides its large bandwidth and low power consumption. This second part of the book, *Mobile and Wireless Communications: Key Technologies and Future Applications*, covers the recent development in ad hoc and sensor networks, the implementation of state of

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the art of wireless transceivers building blocks and recent development on optical wireless communication systems. We hope that this book will be useful for students, researchers and practitioners in their research studies.

Nanoelectronics, Circuits and Communication Systems

Nanoelectronics: Devices, Circuits and Systems explores current and emerging trends in the field of nanoelectronics, from both a devices-to-circuits and circuits-to-systems perspective. It covers a wide spectrum and detailed discussion on the field of nanoelectronic devices, circuits and systems. This book presents an in-depth analysis and description of electron transport phenomenon at nanoscale dimensions. Both qualitative and analytical approaches are taken to explore the devices, circuit functionalities and their system applications at deep submicron and nanoscale levels. Recent devices, including FinFET, Tunnel FET, and emerging materials, including graphene, and its applications are discussed. In addition, a chapter on advanced VLSI interconnects gives clear insight to the importance of these nano-transmission lines in determining the overall IC performance. The importance of integration of optics with electronics is elucidated in the optoelectronics and photonic integrated circuit sections of this book. This book provides valuable resource materials for scientists and electrical engineers who want to learn more about nanoscale electronic materials and how they are used. Shows

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how electronic transport works at the nanoscale level
Demonstrates how nanotechnology can help engineers create more effective circuits and systems
Assesses the most commonly used nanoelectronic devices, explaining which is best for different situations

Reliability of Nanoscale Circuits and Systems

CMOS Processors and Memories addresses the-state-of-the-art in integrated circuit design in the context of emerging computing systems. New design opportunities in memories and processor are discussed. Emerging materials that can take system performance beyond standard CMOS, like carbon nanotubes, graphene, ferroelectrics and tunnel junctions are explored. CMOS Processors and Memories is divided into two parts: processors and memories. In the first part we start with high performance, low power processor design, followed by a chapter on multi-core processing. They both represent state-of-the-art concepts in current computing industry. The third chapter deals with asynchronous design that still carries lots of promise for future computing needs. At the end we present a “hardware design space exploration” methodology for implementing and analyzing the hardware for the Bayesian inference framework. This particular methodology involves: analyzing the computational cost and exploring candidate hardware components, proposing various custom architectures using both traditional CMOS and hybrid nanotechnology CMOL.

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The first part concludes with hybrid CMOS-Nano architectures. The second, memory part covers state-of-the-art SRAM, DRAM, and flash memories as well as emerging device concepts. Semiconductor memory is a good example of the full custom design that applies various analog and logic circuits to utilize the memory cell's device physics. Critical physical effects that include tunneling, hot electron injection, charge trapping (Flash memory) are discussed in detail. Emerging memories like FRAM, PRAM and ReRAM that depend on magnetization, electron spin alignment, ferroelectric effect, built-in potential well, quantum effects, and thermal melting are also described. CMOS Processors and Memories is a must for anyone serious about circuit design for future computing technologies. The book is written by top notch international experts in industry and academia. It can be used in graduate course curriculum.

Nanophotonics

This book comprises the refereed proceedings of the International Conference, AIM/CCPE 2012, held in Bangalore, India, in April 2012. The papers presented were carefully reviewed and selected from numerous submissions and focus on the various aspects of research and development activities in computer science, information technology, computational engineering, mobile communication, control and instrumentation, communication system, power electronics and power engineering.

Nanotechnology for Telecommunications

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In-depth coverage of integrated circuit design on the nanoscale level Written by international experts in industry and academia, CMOS Nanoelectronics addresses the state of the art in integrated circuit design in the context of emerging systems. New, exciting opportunities in body area networks, wireless communications, data networking, and optical imaging are discussed. This cutting-edge guide explores emerging design concepts for very low power and describes design approaches for RF transceivers, high-speed serial links, PLL/DLL, and ADC/DAC converters. CMOS Nanoelectronics covers:

- Portable high-efficiency polar transmitters
- All-digital RF signal generation
- Frequency multiplier design
- Tunable CMOS RF filters
- GaAs HBT linear power amplifier design
- High-speed serial I/O design
- CDMA-based crosstalk cancellation
- Delta-sigma fractional-N PLL
- Delay locked loops
- Digital clock generators
- Analog design in deep submicron CMOS technologies
- 1/f noise reduction for linear analog CMOS ICs
- Broadband high-resolution bandpass sigma-delta modulators
- Analog/digital conversion specifications for power line communication systems
- Digital-to-analog converters for LCDs
- Sub-1-V CMOS bandgap reference design
- And much more

Mobile Communication and Power Engineering

This book bridges the divide between the fields of power systems engineering and computer communication through the new field of power system information theory. Written by an expert with

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With vast experience in the field, this book explores the smart grid from generation to consumption, both as it is planned today and how it will evolve tomorrow. The book focuses upon what differentiates the smart grid from the "traditional" power grid as it has been known for the last century. Furthermore, the author provides the reader with a fundamental understanding of both power systems and communication networking. It shows the complexity and operational requirements of the evolving power grid, the so-called "smart grid," to the communication networking engineer; and similarly, it shows the complexity and operational requirements for communications to the power systems engineer. The book is divided into three parts. Part One discusses the basic operation of the electric power grid, covering fundamental knowledge that is assumed in Parts Two and Three. Part Two introduces communications and networking, which are critical enablers for the smart grid. It also considers how communication and networking will evolve as technology develops. This lays the foundation for Part Three, which utilizes communication within the power grid. Part Three draws heavily upon both the embedded intelligence within the power grid and current research, anticipating how and where computational intelligence will be implemented within the smart grid. Each part is divided into chapters and each chapter has a set of questions useful for exercising the readers' understanding of the material in that chapter. Key Features: Bridges the gap between power systems and communications experts Addresses the smart grid from generation to consumption, both as it is planned today and how it

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will likely evolve tomorrow Explores the smart grid from the perspective of traditional power systems as well as from communications Discusses power systems, communications, and machine learning that all define the smart grid It introduces the new field of power system information theory

CMOS Nanoelectronics: Analog and RF VLSI Circuits

This book describes methodologies in the design of VLSI devices, circuits and their applications at nanoscale levels. The book begins with the discussion on the dominant role of power dissipation in highly scaled devices. The 15 Chapters of the book are classified under four sections that cover design, modeling, and simulation of electronic, magnetic and compound semiconductors for their applications in VLSI devices, circuits, and systems. This comprehensive volume eloquently presents the design methodologies for ultra-low power VLSI design, potential post-CMOS devices, and their applications from the architectural and system perspectives. The book shall serve as an invaluable reference book for the graduate students, Ph.D./ M.S./ M.Tech. Scholars, researchers, and practicing engineers working in the frontier areas of nanoscale VLSI design.

Nanoscale VLSI

This self-contained book addresses the need for analysis, characterization, estimation, and

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optimization of the various forms of power dissipation in the presence of process variations of nano-CMOS technologies. The authors show very large-scale integration (VLSI) researchers and engineers how to minimize the different types of power consumption of digital circuits. The material deals primarily with high-level (architectural or behavioral) energy dissipation.

Internet Networks

Advances in design methods and process technologies have resulted in a continuous increase in the complexity of integrated circuits (ICs). However, the increased complexity and nanometer-size features of modern ICs make them susceptible to manufacturing defects, as well as performance and quality issues. Testing for Small-Delay Defects in Nanoscale CMOS Integrated Circuits covers common problems in areas such as process variations, power supply noise, crosstalk, resistive opens/bridges, and design-for-manufacturing (DfM)-related rule violations. The book also addresses testing for small-delay defects (SDDs), which can cause immediate timing failures on both critical and non-critical paths in the circuit. Overviews semiconductor industry test challenges and the need for SDD testing, including basic concepts and introductory material Describes algorithmic solutions incorporated in commercial tools from Mentor Graphics Reviews SDD testing based on "alternative methods" that explores new metrics, top-off ATPG, and circuit topology-based solutions Highlights the advantages and disadvantages of a diverse set of metrics, and identifies scope for

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improvement Written from the triple viewpoint of university researchers, EDA tool developers, and chip designers and tool users, this book is the first of its kind to address all aspects of SDD testing from such a diverse perspective. The book is designed as a one-stop reference for current industrial practices, research challenges in the domain of SDD testing, and recent developments in SDD solutions.

Journal of Nanoscience and Nanotechnology

This book is intended to give a general overview of reliability, faults, fault models, nanotechnology, nanodevices, fault-tolerant architectures and reliability evaluation techniques. Additionally, the book provides an in depth state-of-the-art research results and methods for fault tolerance as well as the methodology for designing fault-tolerant systems out of highly unreliable components.

High-Frequency Integrated Circuits

Circuits for Emerging Technologies Beyond CMOS New exciting opportunities are abounding in the field of body area networks, wireless communications, data networking, and optical imaging. In response to these developments, top-notch international experts in industry and academia present Circuits at the Nanoscale: Communications, Imaging, and Sensing. This volume, unique in both its scope and its focus, addresses the state-of-the-art in integrated circuit design in the context of emerging systems. A must for

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anyone serious about circuit design for future technologies, this book discusses emerging materials that can take system performance beyond standard CMOS. These include Silicon on Insulator (SOI), Silicon Germanium (SiGe), and Indium Phosphide (InP). Three-dimensional CMOS integration and co-integration with Microelectromechanical (MEMS) technology and radiation sensors are described as well. Topics in the book are divided into comprehensive sections on emerging design techniques, mixed-signal CMOS circuits, circuits for communications, and circuits for imaging and sensing. Dr. Krzysztof Iniewski is a director at CMOS Emerging Technologies, Inc., a consulting company in Vancouver, British Columbia. His current research interests are in VLSI circuits for medical applications. He has published over 100 research papers in international journals and conferences, and he holds 18 international patents granted in the United States, Canada, France, Germany, and Japan. In this volume, he has assembled the contributions of over 60 world-renowned experts who are at the top of their field in the world of circuit design, advancing the bank of knowledge for all who work in this exciting and burgeoning area.

Low Power Circuits for Emerging Applications in Communications, Computing, and Sensing

This textbook provides a thorough and accessible treatment of semiconductor lasers from a design and engineering perspective. It includes both the physics

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of devices as well as the engineering, designing and testing of practical lasers. The material is presented clearly with many examples provided. Readers of the book will come to understand the finer aspects of the theory, design, fabrication and test of these devices and have an excellent background for further study of optoelectronics. This book also: Provides a multi-faceted approach to explaining the theories behind semiconductor lasers, utilizing mathematical examples, illustrations and written theoretical presentations Offers a balance of relevant optoelectronic topics, with specific attention given to distributed feedback lasers, growth techniques and waveguide cavity design Provides a summary of every chapter, worked examples, and problems for readers to solve Incorporates and explains recent breakthroughs in laser design

Nanoscale Transistors

Power-efficient Ultra-high-speed Communication Circuit Techniques in Nanoscale CMOS

Low-Power High-Level Synthesis for Nanoscale CMOS Circuits

Advanced Semiconductor and Organic Nano-techniques: Nanoscale electronics

Where To Download Circuits At The Nanoscale Communications Imaging And Sensing Devices Circuits And Systems **and optoelectronics**

With its unique promise to revolutionize science, engineering, technology, and other fields, nanotechnology continues to profoundly impact associated materials, components, and systems, particularly those used in telecommunications. These developments are leading to easier convergence of related technologies, massive storage data, compact storage devices, and higher-performance computing. Nanotechnology for Telecommunications presents vital technical scientific information to help readers grasp issues and challenges associated with nanoscale telecommunication system development and commercialization—and then avail themselves of the many opportunities to be gleaned. This book provides technical information and research ideas regarding the use of nanotechnology in telecommunications and information processing, reflecting the continuing trend toward the use of optoelectronics. Nanotech will eventually lead to a technology cluster that offers a complete range of functionalities for systems used in domains including information, energy, construction, environmental, and biomedical. Describing current and future developments that hold promise for significant innovations in telecommunications, this book is organized to provide a progressive understanding of topics including: Background information on nanoscience and nanotechnology Specific applications of nanotechnology in telecommunications Nanostructured optoelectronic materials MEMS, NEMS, and their applications in communication

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systems Quantum dot Cellular Automata (QCA) and its applications in telecommunication systems How nonohmic nonlinear behavior affects both digital and analog signal processing Concepts regarding quantum switching and its applications in quantum networks The scale of the physical systems that use nanoscale electronic devices is still large, and that presents serious challenges to the establishment of interconnections between nanoscale devices and the outside world. Also addressing consequent social implications of nanotech, this book reviews a broad range of the nano concepts and their influence on every aspect of telecommunications. It describes the different levels of interconnections in systems and details the standardized assembly process for a broad specrum of micro-, nano-, bio-, fiber-optic, and optoelectronic components and functions. This book is a powerful tool for understanding how to harness the power of nanotech through integration of materials, processes, devices, and applications.

Medical Imaging

There is a growing need to understand and combat potential radiation damage problems in semiconductor devices and circuits. Assessing the billion-dollar market for detection equipment in the context of medical imaging using ionizing radiation, Electronics for Radiation Detection presents valuable information that will help integrated circuit (IC) designers and other electronics professionals take full advantage of the tremendous developments and opportunities associated with this burgeoning field.

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Assembling contributions from industrial and academic experts, this book— Addresses the state of the art in the design of semiconductor detectors, integrated circuits, and other electronics used in radiation detection Analyzes the main effects of radiation in semiconductor devices and circuits, paying special attention to degradation observed in MOS devices and circuits when they are irradiated Explains how circuits are built to deal with radiation, focusing on practical information about how they are being used, rather than mathematical details Radiation detection is critical in space applications, nuclear physics, semiconductor processing, and medical imaging, as well as security, drug development, and modern silicon processing techniques. The authors discuss new opportunities in these fields and address emerging detector technologies, circuit design techniques, new materials, and innovative system approaches. Aimed at postgraduate researchers and practicing engineers, this book is a must for those serious about improving their understanding of electronics used in radiation detection. The information presented here can help you make optimal use of electronic detection equipment and stimulate further interest in its development, use, and benefits.

Introduction to Nanoscale Science and Technology

A must-read for anyone working in electronics in the healthcare sector This one-of-a-kind book addresses state-of-the-art integrated circuit design in the

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context of medical imaging of the human body. It explores new opportunities in ultrasound, computed tomography (CT), magnetic resonance imaging (MRI), nuclear medicine (PET, SPECT), emerging detector technologies, circuit design techniques, new materials, and innovative system approaches. Divided into four clear parts and with contributions from a panel of international experts, Medical Imaging systematically covers: X-ray imaging and computed tomography–X-ray and CT imaging principles; Active Matrix Flat Panel Imagers (AMFPI) for diagnostic medical imaging applications; photon counting and integrating readout circuits; noise coupling in digital X-ray imaging Nuclear medicine–SPECT and PET imaging principles; low-noise electronics for radiation sensors Ultrasound imaging–Electronics for diagnostic ultrasonic imaging Magnetic resonance imaging–Magnetic resonance imaging principles; MRI technology

Electronics for Radiation Detection

This book describes the design of optical receivers that use the most economical integration technology, while enabling performance that is typically only found in very expensive devices. To achieve this, all necessary functionality, from light detection to digital output, is integrated on a single piece of silicon. All building blocks are thoroughly discussed, including photodiodes, transimpedance amplifiers, equalizers and post amplifiers.

The British National Bibliography

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This book is loosely based on a Multidisciplinary University Research Initiative (MURI) project and a few supplemental projects sponsored by the Office of Naval Research (ONR) during the time frame of 2004–2009. The initial technical scope and vision of the MURI project was formulated by Drs. Larry Cooper and Joel Davis, both program officers at ONR at the time. The unifying theme of this MURI project and its companion efforts is the concept of cellular nonlinear/neural network (CNN) technology and its various extensions and chip implementations, including nanoscale sensors and the broadening field of cellular wave computing. In recent years, CNN-based vision system drew much attention from vision scientists to device technologists and computer architects. Due to its early implementation in a two-dimensional (2D) topography, it found success in early vision technology applications, such as focal-plane arrays, locally adaptable sensor/ processor integration, resulting in extremely high frame rates of 10,000 frames per second. More recently it drew increasing attention from computer architects, due to its intrinsic local interconnect architecture and parallel processing paradigm. As a result, a few spin-off companies have already been successful in bringing cellular wave computing and CNN technology to the market. This book aims to capture some of the recent advances in the field of CNN research and a few select areas of applications.

Nanoelectronics

To push MOSFETs to their scaling limits and to explore

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devices that may complement or even replace them at molecular scale, a clear understanding of device physics at nanometer scale is necessary. Nanoscale Transistors provides a description on the recent development of theory, modeling, and simulation of nanotransistors for electrical engineers, physicists, and chemists working on nanoscale devices. Simple physical pictures and semi-analytical models, which were validated by detailed numerical simulations, are provided for both evolutionary and revolutionary nanotransistors. After basic concepts are reviewed, the text summarizes the essentials of traditional semiconductor devices, digital circuits, and systems to supply a baseline against which new devices can be assessed. A nontraditional view of the MOSFET using concepts that are valid at nanoscale is developed and then applied to nanotube FET as an example of how to extend the concepts to revolutionary nanotransistors. This practical guide then explore the limits of devices by discussing conduction in single molecules

Comprehensive Nanoscience and Technology

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